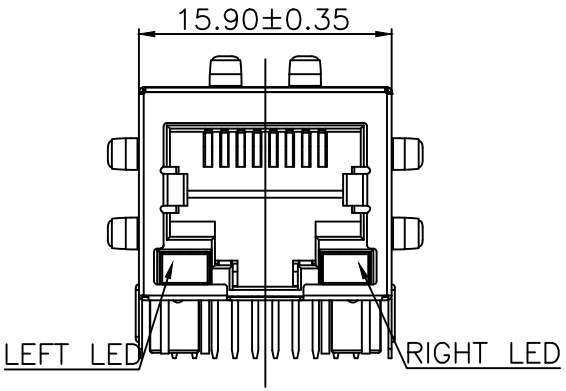
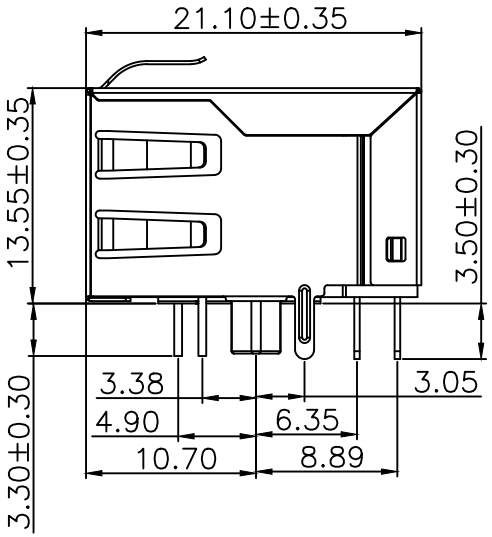
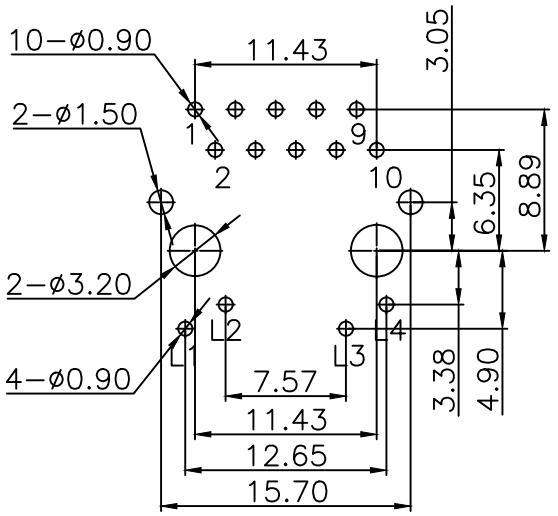
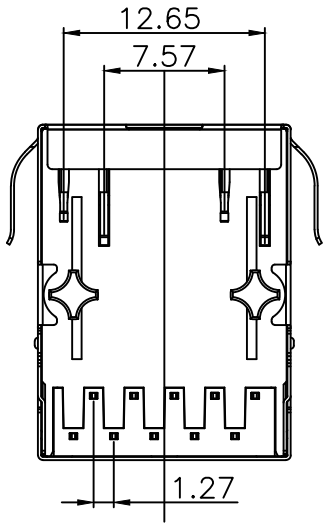
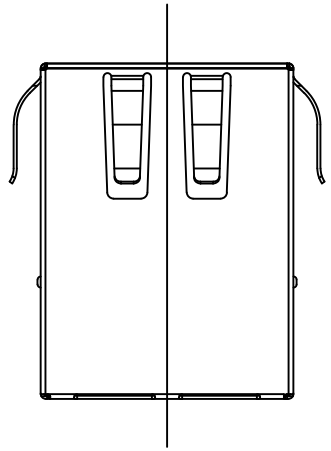






Recommended PCB Layout
(Tolerance:±0.05)



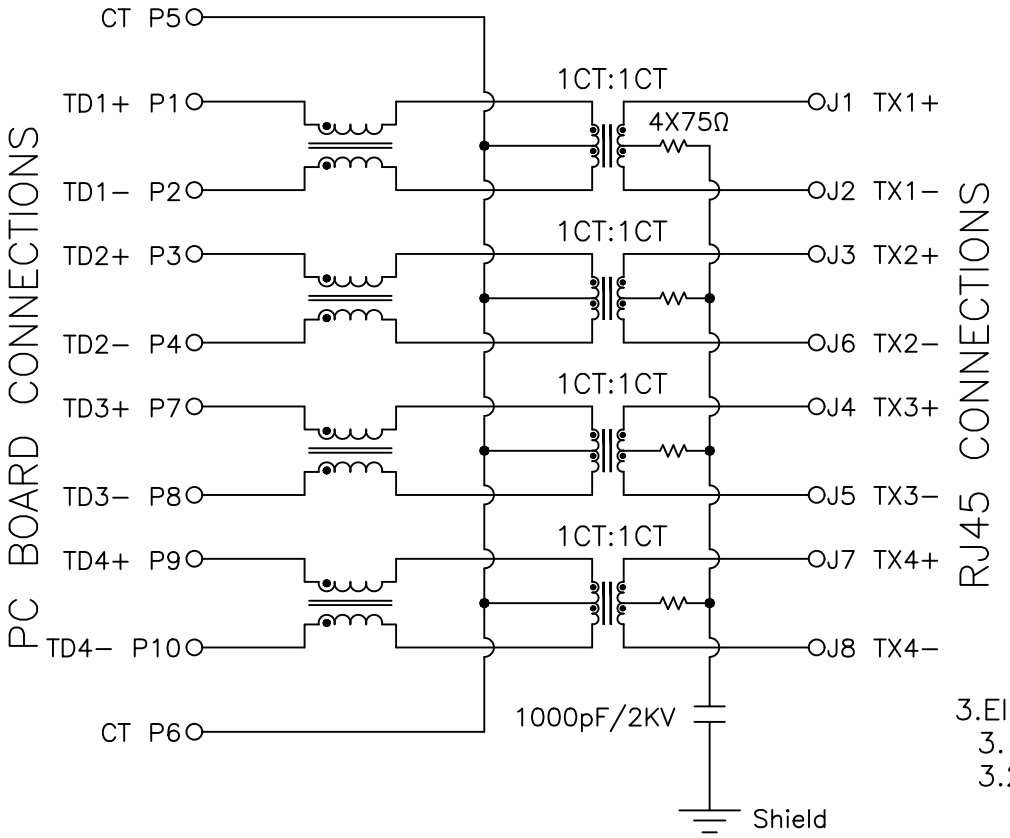


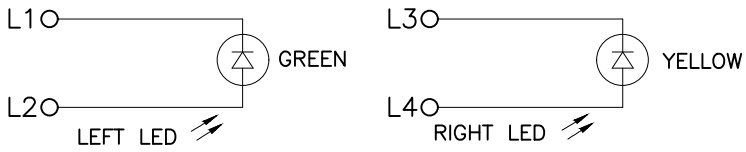
1.Material:
1.1 Housing: PBT UL94V-0 Black.
Insert: PBT UL94V-0 Black.
1.2 Shell: 30u" ~50u" Ni Over Stainless steel(SUS 201), T=0.20mm.
1.3 RJ45 Terminal:Phosphor Bronze(C5191) T=0.35mm
Plating:6u" Gold Plated On Contact Area,
80u" ~120u" Tin Plated On Solder Area,
30u" ~50u" Ni Underplated Overall.
1.4 Tail Terminal: Brass(C2680) T=0.35mm,
Plating: 80u" ~120u" Tin Plated Over,
30u" ~50u" Ni Underplated Overall.
2.Environmental requirements:
2.1 Lead-free process,compliant RoHS.

裕 泰电子有限公司				GENERAL TOLERANCE		DESIGNED: Joan.Zhao	DATE: 2025.08.29
				X.°±5.0°	X.±0.38		
				.X°±3.0°	.X±0.30	CHECKED: Joan.Zhao	DATE: 2025.08.29
				.XX°±2.0°	.XX±0.25		
REV.	UNIT.	SCALE.	PAGE.	.XXX°±1.0°	.XXX±0.15	APPROVED: Sunny.F	DATE: 2025.08.29
A	mm	1:1	1/2				

DWG. NO.	YL2G5J001A
PART NO.	YL2G5J001A

TITLE.
RJ45 1x1 Dip Tab Down
2.5G Base With LED 6u" 3S





Emitting Color	λp(nm)	Vf@If=20mA
Green	565	1.8−2.5V
YELLOW	585	1.8−2.5V

3.Electrical Specification @25°C:
- 3.1 Inductance: 180uH Min With 8mA DC bias.
- 3.2 Insertion loss :1−50MHZ −0.5dB max.
  50−125MHZ −1dB max.
  125−200MHZ −2dB max.
- 3.3 Return loss : 1−40MHZ −20dB min.
  40−200MHZ −20+15*log(Freq MHz/40MHz) dB min.
- 3.4 Common Mode Rejection:1−50MHZ −35dB min.
  125MHZ −30dB min.
  200MHZ −25dB min.
- 3.5 Cross Talk:1−40MHZ −35dB min.
  40~125MHz −35+15log((Freq MHz/40MHz)) dB min.

4.Operating and Storage Temperature:
- 4.1 Operating Temperature : −40°C to +85°C.
- 4.2 Storage Temperature : −40°C to +85°C.

5.Wave soldering peak temperature:260°C.

裕 泰 电 子 有 限 公 司				GENERAL TOLERANCE		DESIGNED: Joan.Zhao	DATE: 2025.08.29	DWG. NO. YL2G5J001A	TITLE. RJ45 1x1 Dip Tab Down 2.5G Base With LED 6u" 3S
				X.°±5.0°	X.±0.38				
				.X°±3.0°	.X±0.30	CHECKED: Joan.Zhao	DATE: 2025.08.29		
				.XX°±2.0°	.XX±0.25			PART NO. YL2G5J001A	
REV.	UNIT.	SCALE.	PAGE.	.XXX°±1.0°	.XXX±0.15	APPROVED: Sunny.F	DATE: 2025.08.29		
A	mm	1:1	2/2						