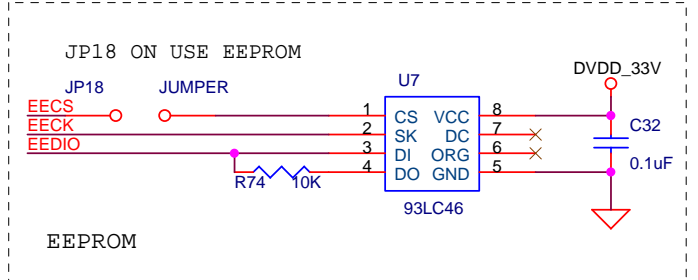
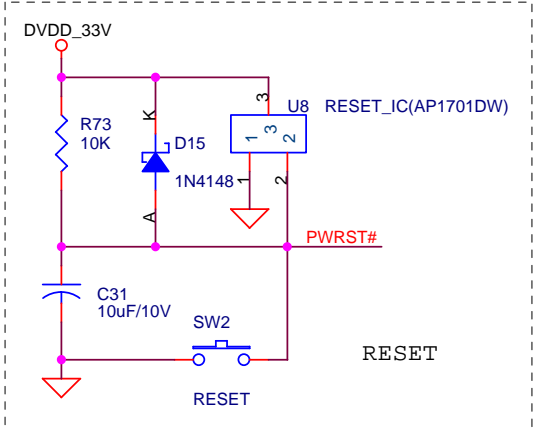
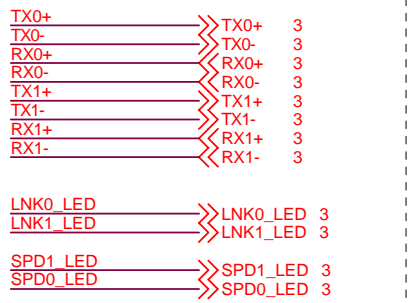
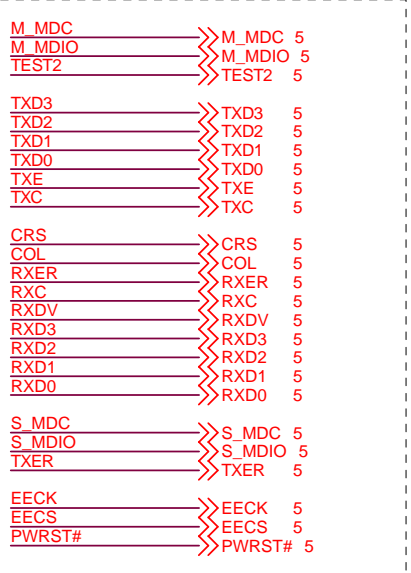
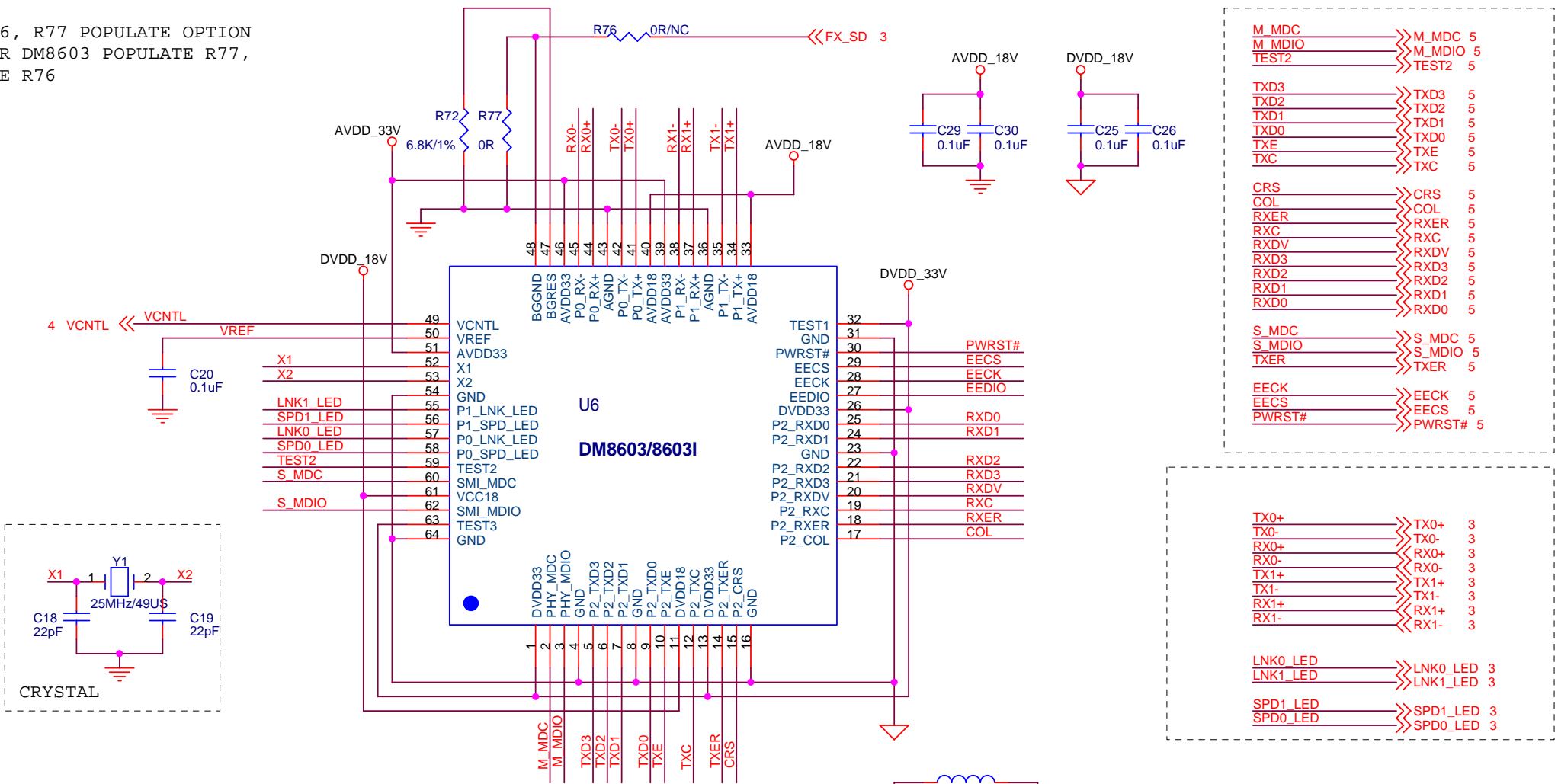


2 PORTS + MII DEMO BOARD V1.0
 Davicom Semiconductor Inc.
 P/N: D2L-8603-E-01-1

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R76, R77 POPULATE OPTION
FOR DM8603 POPULATE R77,
DNE R76



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TP / LED Interface

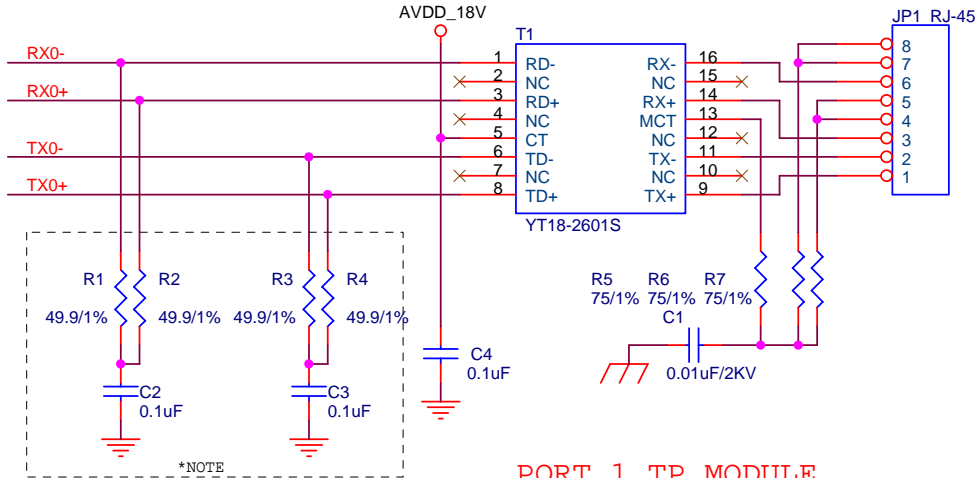


WHEN USE FIBER MODULE, MUST CHANGE DM8603 PART OF THE TP CONPONET

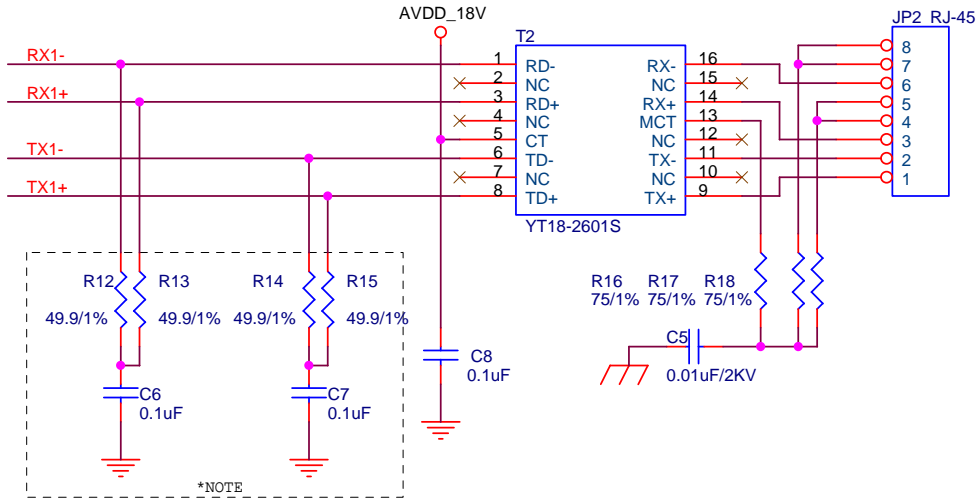
R1	83R/1%
R2	83R/1%
R3	182R/1%
R4	182R/1%
R12	83R/1%
R13	83R/1%
R14	182R/1%
R15	182R/1%
C2	0R
C3	0R
C14	0R
C15	0R

*NOTE

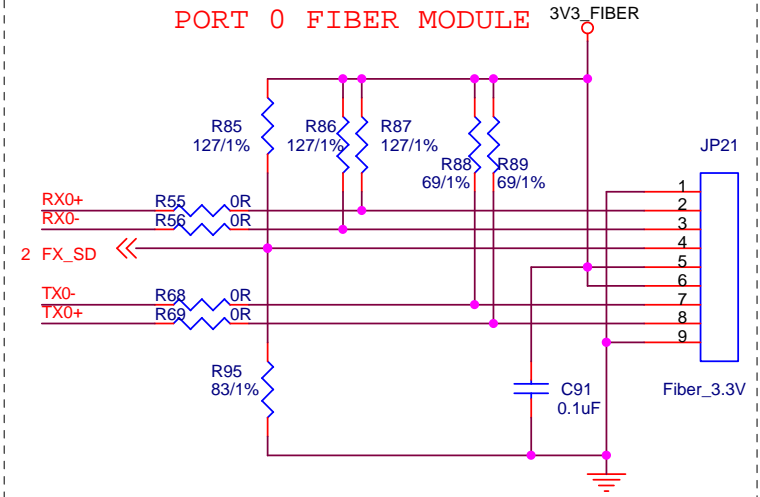
PORT 0 TP MODULE



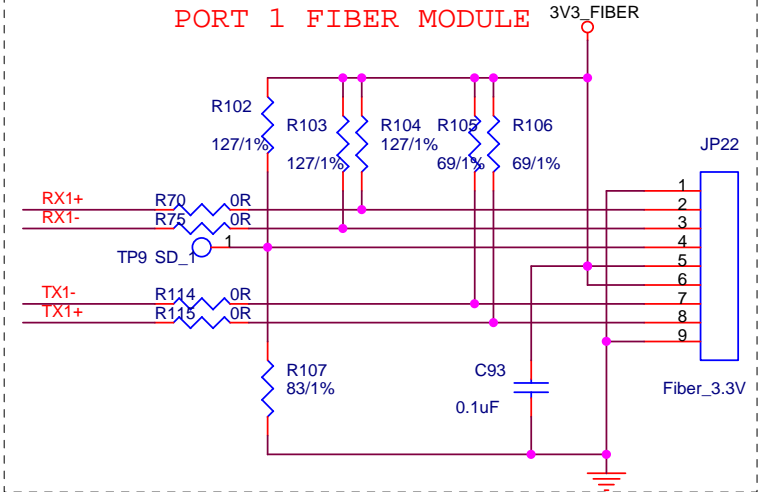
PORT 1 TP MODULE



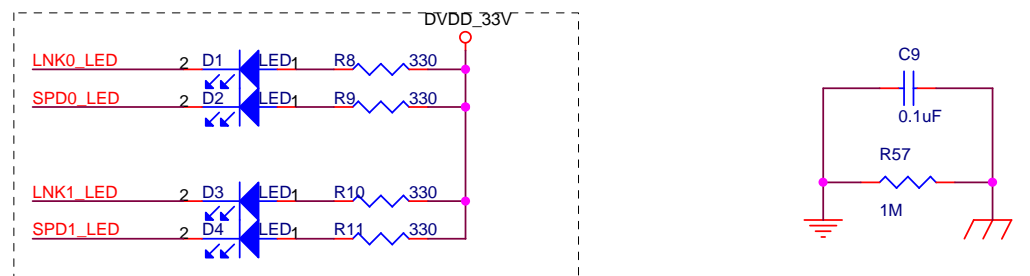
PORT 0 FIBER MODULE



PORT 1 FIBER MODULE

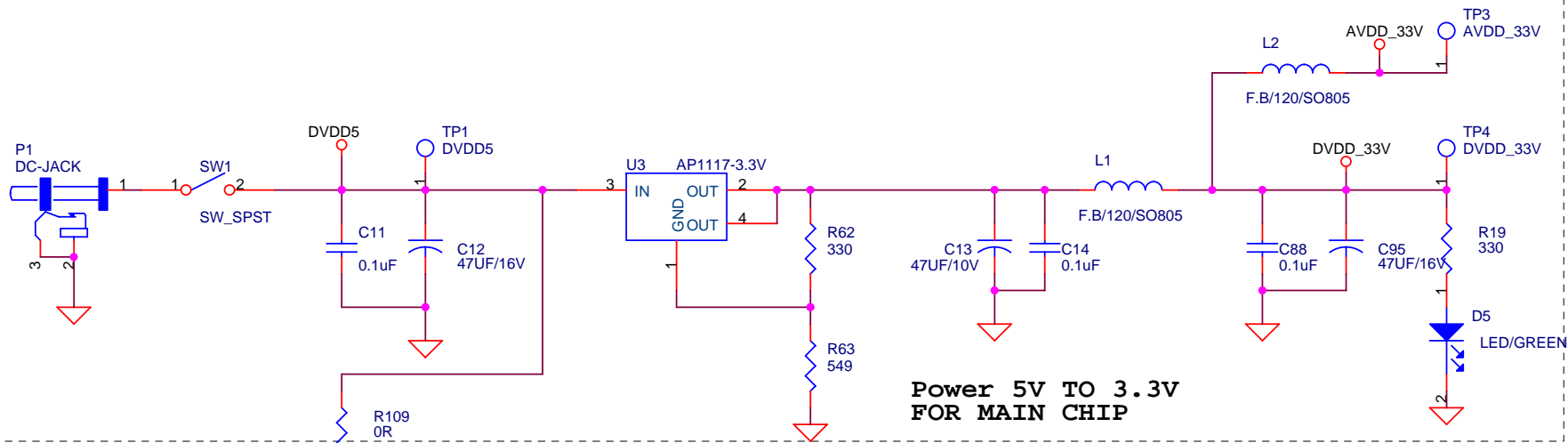


MAIN CHIP LEDS

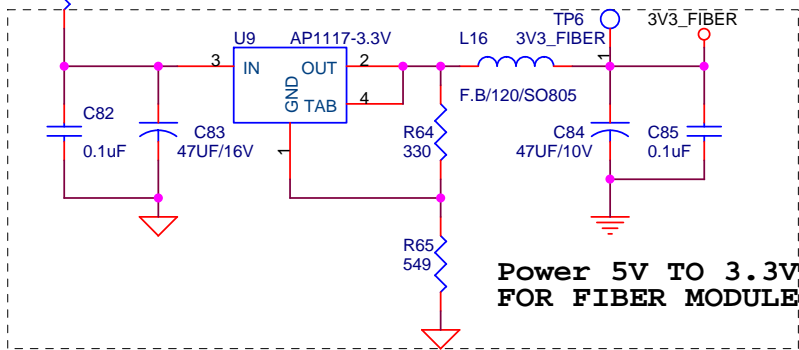


Davicom Semiconductor Inc

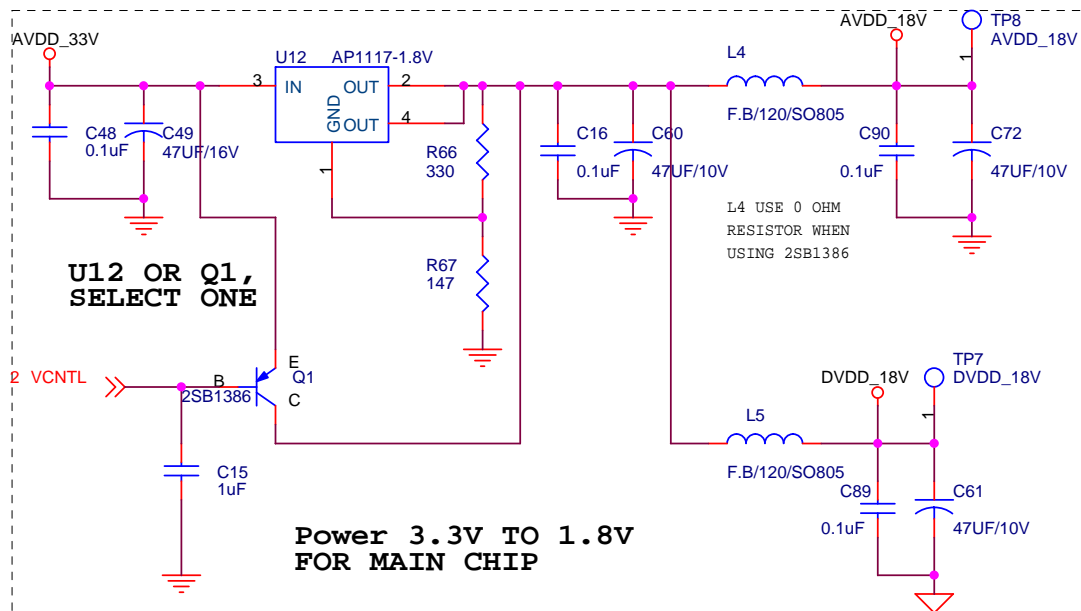
Title		
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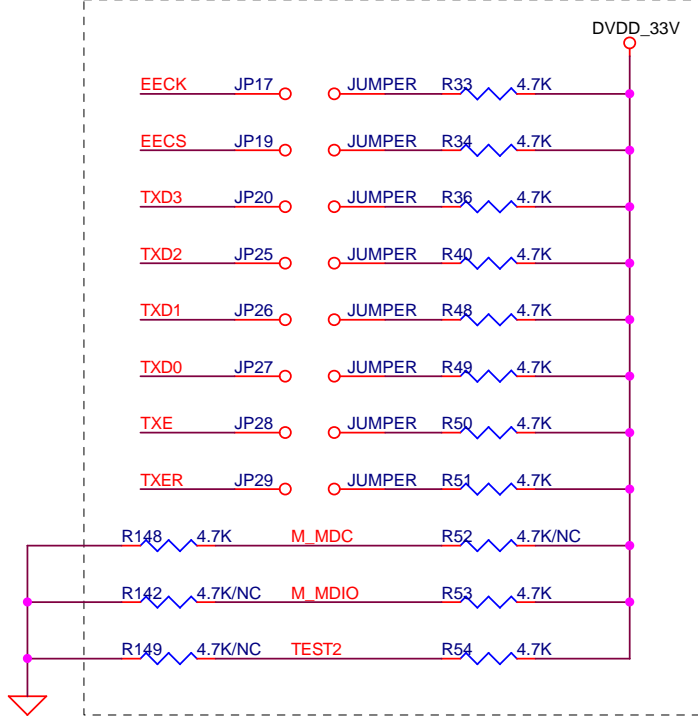
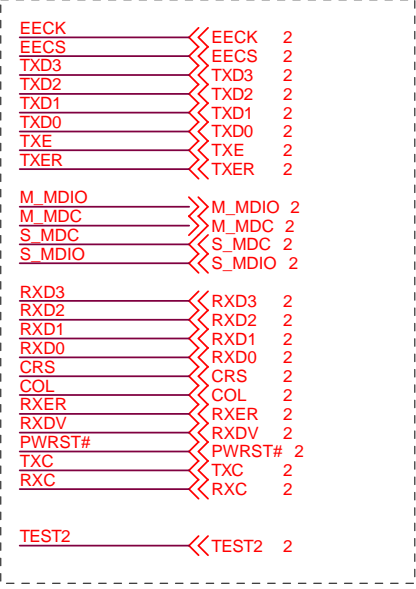
ADJUSTABLE LDO CALCULATION
 $V_{out} = V_{ref} \times (1 + R63 / R62)$
 $V_{ref} = 1.25 \text{ V}$
 For fixed V_{out} LDO,
 $R62, R64 = \text{open}, R63, R65 = 0 \text{ ohm}$



ADJUSTABLE LDO CALCULATION
 $V_{out} = V_{ref} \times (1 + R67 / R66)$
 $V_{ref} = 1.25 \text{ V}$
 For fixed V_{out} LDO,
 $R66 = \text{open}, R67 = 0 \text{ ohm}$

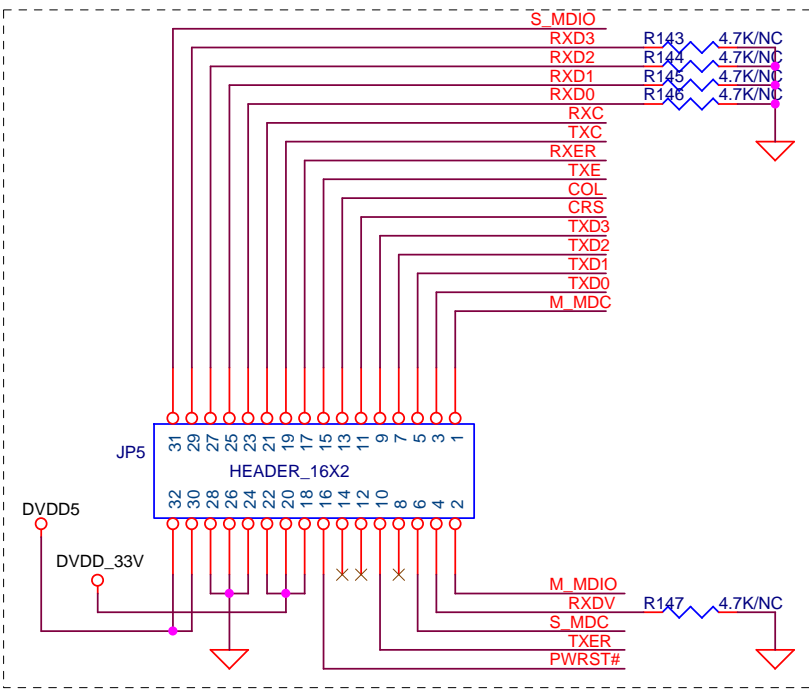


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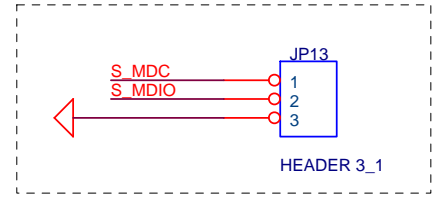
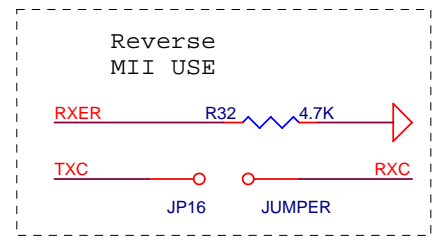


MII Mode Strap Pins

EECK : 0= P2 force 10M mode DEFAULT
 1= P2 force 100M mode
 EECS : 0= p0 in copper mode DEFAULT
 1= p0 in Fiber mode
 TXE : 0= Port 2 normal mode DEFAULT
 1= Port 2 force mode
 TXER : 0= p1 in copper mode DEFAULT
 1= p1 in Fiber mode
 TXD0 : TEST ID 0,1,2,3 DEFAULT 0
 TXD1 : TEST ID 0,1,2,3 DEFAULT 0
 TXD3 TXD2 | PORT 2 INTERFACE DEFAULT
 0 0 | MII
 0 1 | Reverse MII
 1 0 | RMI
 1 1 | Reserved
 M_MDC : TEST MODE DEFAULT 0



MII INTERFACE CONNECTOR



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VER	DATE	ENGINEER	NOTE
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1.0	09/21/2010	Willie Niou	new circuit
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D

D

C

C

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B

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Davicom Semiconductor Inc.		
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