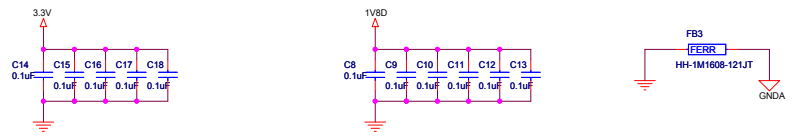
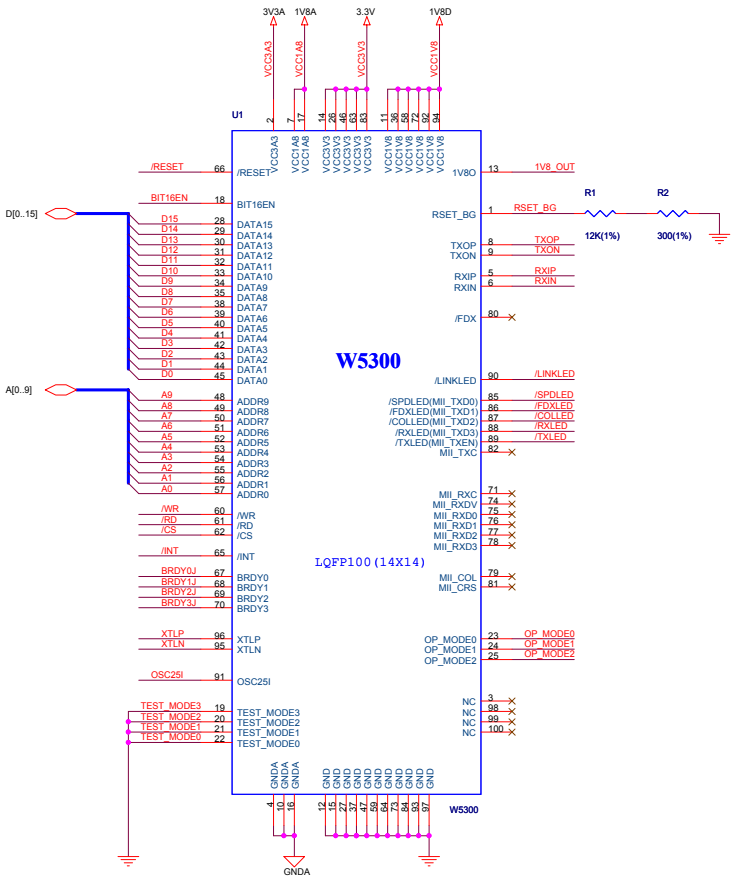
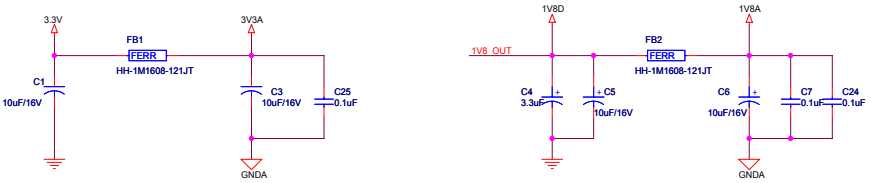


The analog and digital ground planes should be as large and intact as possible. If the ground plane is large enough, the analog and digital grounds can be separated, which is the ideal configuration. However, if the total ground plane is not sufficiently large, partition of the ground plane is not a good idea. In this case, all the ground pins can be connected together to a larger single and intact ground plane. (remove FB3, and change 'GNDA' to 'GND'.)

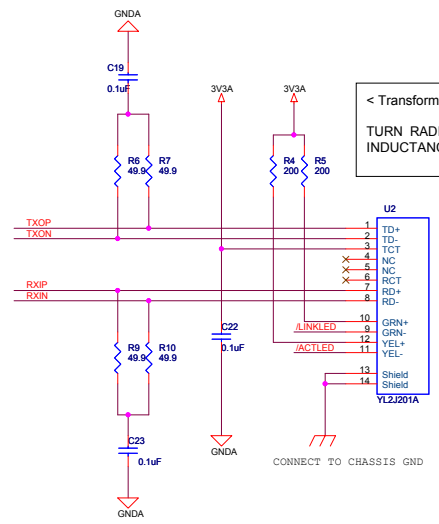
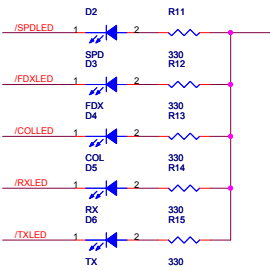
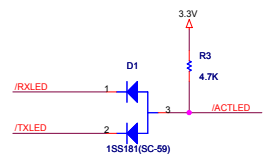


Place C8, C9, C10, C11, C12, C13, C14, C15, C16, C17, C18 as close to each power pin as possible.



Place FB1, C1, C3, C25 as close to each power pin as possible.

Place C4, C5, FB2 close to 1V8_OUT and place C6, C7, C24 close to 1V8A pin.



< Transformer Specification >
TURN RATIO : TX&RX = 1CT:1CT
INDUCTANCE : 350uH MIN.

NOTE: The contact windows of buying the transformer YL2J201A is listed as below:
YuTai Electronics Co., LTD
Name: Victor Luo
TEL: 0086-574-63620701, 63621610
EMAIL: mico_yu@yeah.net
Http://www.yutai-elec.com

CLOCK SOURCE OPTION
(You should adopt only one option.)

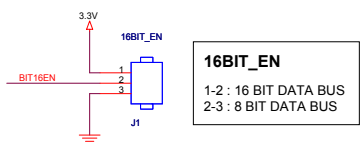
<OPTION1 : 2 CRYSTAL SOURCE>

<OPTION2 : Oscillator SOURCE 1>

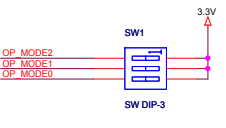
When using oscillator, be sure to use 1.8V level oscillator and connect only to XTLP, and let be float XTLN.

<OPTION3 : Oscillator SOURCE 2>

In order to prevent the leakage current, be sure to keep XTLP high and float XTLN, and use 1.8V level oscillator.



16BIT_EN
1-2: 16 BIT DATA BUS
2-3: 8 BIT DATA BUS



SW1
000 : Auto-negotiation enable with all capabilities
001 : Auto-negotiation with 100 BASE-TX FDX/HDX ability
010 : Auto-negotiation with 10 BASE-T FDX/HDX ability
011 : Reserved
100 : Manual selection of 100 BASE-TX FDX
101 : Manual selection of 100 BASE-TX HDX
110 : Manual selection of 10 BASE-T FDX
111 : Manual selection of 10 BASE-T HDX

< DIFFERENCES BETWEEN VER.1.1 and VER.1.2 >

1. Changed values of C20 and C21 in CRYSTAL circuit from 18pF to 13pF. It can be better performed 'auto-negotiation' function.
2. Reinforced power circuit.(refer to below table)
 - Remove component : C2
 - Add component : C24, C25
 - Change component : FB1, FB2, FB3

	Rev1.1	Rev1.2
Clock circuit	<p style="text-align: center;"><OPTION1 : 2 CRYSTAL SOURCE></p>	<p style="text-align: center;"><OPTION1 : 2 CRYSTAL SOURCE></p>
3.3V circuit		
1.8V circuit		
1.8V circuit		

Revision History		
File	Document Number	Rev
Size	<Doc>	1.2
C		
Date: Saturday, August 11, 2012	Sheet 1 of 1	